



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/715,054

Confirmation No.

Applicant: James Y. Cho et al.

Filed: 11/17/2003

Art Unit: 2181

Examiner: Not Assigned

Title: Bus Sampling On One Edge Of A Clock Signal and Driving On Another Edge

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant submits the attached Information Disclosure Statement and requests the Examiner to consider the listed references. All of the references noted on this IDS were submitted on Form PTO-1449 by the Applicant or cited by the Examiner in the parent case, Application No. 09/680,523; filed October 6, 2000. Thus, copies of the references are in the parent case.

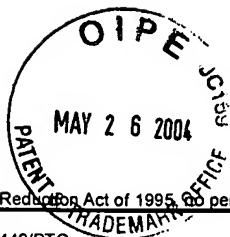
It is certified, to the best of knowledge, this IDS is being submitted before the mailing date of a first Office Action on the merits. However, if there are any fee shortages related to this submission, please charge such fee shortages to Deposit Account No. 50-2126.

Respectfully submitted,

GARLICK, HARRISON & MARKISON, LLP
(Customer No. 34,399)

Date: 5-24-2004

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PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/715,054
Filing Date	11/17/2003
First Named Inventor	James Y. Cho
Art Unit	2181
Examiner Name	
Attorney Docket Number	BP1745CON

Sheet 1 of 3

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 2002/0147875	10-2002	Singh et al.	
		US- 6,378,017	04-2002	Girzon et al.	
		US- 6,442,642	08-2002	Brooks, John Milford	
		US- 6,418,491	07-2002	Martin San Juan, M.	
		US- 2001/0034802	10-2001	Peng et al.	
		US- 6,378,017	04-2002	Girzon et al.	
		US- 6,173,349	01-2001	Qureshi et al.	
		US- 6,442,644	08-2002	Gustavson et al.	
		US- 5,919,265	07/1999	Nishtala et al.	
		US- 6,078,546	06/2000	Lee	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ³
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/715,054
				Filing Date	11/17/2003
				First Named Inventor	James Y. Cho
				Art Unit	2181
				Examiner Name	
Sheet	2	of	3	Attorney Docket Number	BP1745CON

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Power PC 601; RISC Microprocessor User's Manual," IBM Microelectronics, Power PC, Motorola, Rev.1, 1993, 8 pages.	
		SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm , January 15, 2001, 2 pages.	
		SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm , January 15, 2001, 3 pages.	
		SiByte, "The Mercurian Processor," http://sibyte.com/mercurian , January 15, 2001, 2 pages.	
		SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.	
		SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.	
		Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.	
		Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.	
		Halfhill, "SiByte Reveals 64-Bit Core for NPUs," Microprocessor Report, June 2000, pp. 45-48.	
		Pentium Pro Family Developer's Manual, Vol. 1: Specifications, Chapter 3, pp. 1-25, 1996.	

Examiner Signature		Date Considered	
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First Named Inventor	James Y. Cho
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